

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A semiconductor device having a D/A converter, the D/A converter comprising:

four signal lines for supplying 2-bit digital signal;

four gradation voltage lines;

four first circuits, wherein both ends of each of the first circuits are connected to ~~[[each]]~~ a corresponding one of the gradation voltage lines ~~in parallel~~,

wherein each of the first circuits comprises two P-channel transistors connected in series to each other and two N-channel transistors connected in series to each other,

wherein the two P-channel transistors and two N-channel transistors are arranged in a line.

2. (Currently Amended) A semiconductor device according to claim 1, wherein the semiconductor device is a liquid crystal display device.

3. (Currently Amended) A semiconductor device according to claim 1, wherein the semiconductor device is an electro-luminescence display device.

4. (Currently Amended) ~~[[A]]~~ An electric equipment having the semiconductor device according to claim 1, ~~wherein the semiconductor device is used in an electronic equipment~~ the electric equipment is selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.

5. (Currently Amended) A semiconductor device having a D/A converter, the D/A converter comprising:

eight signal lines for supplying 4-bit digital signal;

sixteen gradation voltage lines;

sixteen first circuits, wherein both ends of each of the first circuits are connected to [[each]] a corresponding one of the gradation voltage lines ~~in parallel~~,

wherein each of the first circuits comprises four P-channel transistors connected in series to each other and four N-channel transistors connected in series to each other,

wherein the four P-channel transistors and four N-channel transistors are arranged in a line.

6. (Currently Amended) A semiconductor device according to claim 5, wherein the semiconductor device is a liquid crystal display device.

7. (Currently Amended) A semiconductor device according to claim 5, wherein the semiconductor device is an electro-luminescence display device.

8. (Currently Amended) [[A]] An electric equipment having the semiconductor device according to claim 5, wherein ~~the semiconductor device is used in an electronic equipment~~ the electric equipment is selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.

9. (Currently Amended) A semiconductor device having a D/A converter, the D/A converter comprising:

2n number of signal lines for supplying n-bit digital signal (n is an integer not less than 2);

[[2n]]  $2^n$  number of gradation voltage lines;

[[2n]]  $2^n$  number of first circuits, wherein both ends of each of the first circuits are connected to a corresponding one of the [[2n]]  $2^n$  number of gradation voltage lines, respectively, wherein each of the first circuits comprises n number of P-channel transistors connected in series to each other and n number of N-channel transistors connected in series to each other; and

an output line connected to [[2n]]  $2^n$ -th first circuit,

wherein the n number of P-channel transistors and n number of N-channel transistors are arranged in a line.

10. (Currently Amended) A semiconductor device according to claim 9, wherein the n-bit digital signal is supplied from latch circuits.

11. (Currently Amended) A semiconductor device according to claim 9, wherein the 2n number of signal lines are connected to the gate electrodes of the n number of P-channel transistors and the n number of N-channel transistors, respectively.

12. (Currently Amended) A semiconductor device according to claim 9, wherein the P-channel transistors and the N-channel transistors comprise a crystalline semiconductor layer formed over a substrate.

13. (Currently Amended) A semiconductor device according to claim 9, wherein the D/A converter is formed over an insulating substrate having a pixel portion thereon.

14. (Currently Amended) A semiconductor device according to claim 9, wherein the semiconductor device is a liquid crystal display device.

15. (Currently Amended) A semiconductor device according to claim 9, wherein the semiconductor device is an electro-luminescence display device.

16. (Currently Amended) ~~[[A]]~~ An electric equipment having the semiconductor device according to claim 9, wherein ~~the semiconductor device is used in an electronic equipment~~ the electric equipment is selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.

17. (Currently Amended) A semiconductor device having a D/A converter, comprising:

2n number of signal lines for supplying n-bit digital signal (n is an integer not less than 2);

~~[[2n]]~~ 2<sup>n</sup> number of gradation voltage lines;

~~[[2n]]~~ 2<sup>n</sup> number of first circuits, each of the first circuits comprising a second circuit comprising n number of P-channel transistors connected in series to each other and a third circuit comprising n number of N-channel transistors connected in series to each other, wherein the second circuit and the third circuit are connected in series to each other and wherein both ends of each of the first circuits are connected to ~~[[each]]~~ a corresponding one of the gradation voltage lines ~~in parallel~~; and

an output line connected to a connection portion between the second circuit and the third circuit in each of the first circuits,

wherein the n number of P-channel transistors and n number of N-channel transistors are arranged in a line.

18. (Currently Amended) A semiconductor device according to claim 17, wherein the n-bit digital signal is supplied from latch circuits.

19. (Currently Amended) A semiconductor device according to claim 17, wherein the 2n number of signal lines are connected to the gate electrodes of the n number of P-channel transistors and the n number of N-channel transistors, respectively.

20. (Currently Amended) A semiconductor device according to claim 17, wherein the P-channel transistors in the second circuit are formed by using a first semiconductor island, and the N-channel transistors in the second are formed by using a second semiconductor island.

21. (Currently Amended) A semiconductor device according to claim 17, wherein the D/A converter is formed over an insulating substrate having a pixel portion thereon.

22. (Currently Amended) A semiconductor device according to claim 17, wherein the semiconductor device is a liquid crystal display device.

23. (Currently Amended) A semiconductor device according to claim 17, wherein the semiconductor device is an electro-luminescence display device.

24. (Currently Amended) ~~[[A]] An electric equipment having the semiconductor device according to claim 17, wherein the semiconductor device is used in an electronic equipment~~ the electric equipment is selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car

navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.

25. (Currently Amended) A semiconductor device having a D/A converter, comprising:

2n number of signal lines for supplying n-bit digital signal (n is an integer not less than 2);

2<sup>n</sup> number of gradation voltage lines; and

2<sup>n</sup> number of first circuits, each of the first circuits comprising a second circuit comprising n number of P-channel transistors connected in series to each other and a third circuit comprising n number of N-channel transistors connected in series to each other,

wherein the second circuit and the third circuit are connected in series to each other, and wherein both ends of each of the first circuits are connected to [each] a corresponding one of the gradation voltage lines in parallel, and

wherein in adjacent two first circuits, an arrangement of the second circuit and the third circuit is reversed between the adjacent first circuits,

wherein the n number of P-channel transistors and n number of N-channel transistors are arranged in a line.

26. (Currently Amended) A semiconductor device according to claim 25, wherein the n-bit digital signal is supplied from latch circuits.

27. (Currently Amended) A semiconductor device according to claim 25, wherein the 2n number of signal lines are connected to the gate electrodes of the n number of P-channel transistors and the n number of N-channel transistors, respectively.

28. (Currently Amended) A semiconductor device according to claim 25, wherein the P-channel transistors in the second circuit are formed by using a first semiconductor island, and the N-channel transistors in the second are formed by using a second semiconductor island.

29. (Currently Amended) A semiconductor device according to claim 25, wherein the D/A converter is formed over an insulating substrate having a pixel portion thereon.

30. (Currently Amended) A semiconductor device according to claim 25, wherein the semiconductor device is a liquid crystal display device.

31. (Currently Amended) A semiconductor device according to claim 25, wherein the semiconductor device is an electro-luminescence display device.

32. (Currently Amended) ~~[[A]] An electric equipment having the semiconductor~~ device according to claim 25, ~~wherein the semiconductor device is an electronic equipment~~ the electric equipment is selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.

33. (Currently Amended) A semiconductor device having a D/A converter, comprising:

2n number of signal lines for supplying n-bit digital signal (n is an integer not less than 2);

[[2n]] 2<sup>n</sup> number of gradation voltage lines; and

[[2n]]  $2^n$  number of first circuits, each of the first circuits comprising a second circuit comprising n number of P-channel transistors connected in series to each other and a third circuit comprising n number of N-channel transistors connected in series to each other,

wherein the second circuit and the third circuit are connected in series to each other, and wherein both ends of each of the first circuit is circuits are connected to [[each]] a corresponding one of the gradation voltage lines ~~in parallel~~, and

wherein voltages supplied to the [[2n]]  $2^n$  number of gradation voltage lines become high in a direction from the first gradation voltage line to the [[2n]]  $2^n$ -th gradation voltage line,

wherein the n number of P-channel transistors and n number of N-channel transistors are arranged in a line.

34. (Currently Amended) A semiconductor device according to claim 33, wherein the x-th ( $1 \leq x \leq$  [[2n]]  $2^n$ ; x is an integer) gradation voltage line and the ([[2n]]  $2^n + 1 - x$ )-th gradation voltage line are paired and are adjacent to each other.

35. (Currently Amended) A semiconductor device according to claim 33, wherein the n-bit digital signal is supplied from latch circuits.

36. (Currently Amended) A semiconductor device according to claim 33, wherein the 2n number of signal lines are connected to the gate electrodes of the n number of P-channel transistors and the n number of N-channel transistors, respectively.

37. (Currently Amended) A semiconductor device according to claim 33, wherein the P-channel transistors in the second circuit are formed by using a first



semiconductor island, and the N-channel transistors in the second are formed by using a second semiconductor island.

38. (Currently Amended) A semiconductor device according to claim 33, wherein the D/A converter is formed over an insulating substrate having a pixel portion thereon.

39. (Currently Amended) A semiconductor device according to claim 33, wherein the semiconductor device is a liquid crystal display device.

40. (Currently Amended) A semiconductor device according to claim 33, wherein the semiconductor device is an electro-luminescence display device.

41. (Currently Amended) [[A]] An electric equipment having the semiconductor device according to claim 33, wherein ~~the semiconductor device is an electronic~~ the electric equipment is equipment selected from the group consisting of a video camera, a still camera, a projector, a projection TV, a head mount display, a car navigation system, a personal computer, a note-sized computer, a portable information terminal, a mobile computer, and a portable telephone.